EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	8	@ad<"20030723" and (hash) and signature and (data near (search$3 retriev$4)) and 713/181.ccls.	US-PGPUB; USPAT	OR	OFF	2008/01/07 05:51
S1	170	@ad<"20030723" and (hash$3 adj memory)	US-PGPUB; USPAT	OR	OFF	2006/08/11 10:58
S2	65	@ad<"20030723" and (hash$3 adj memory) and signature	US-PGPUB; USPAT	OR	OFF	2006/08/11 10:59
S3	51	@ad<"20030723" and (hash$3 adj memory) and signature and index	US-PGPUB; USPAT	OR	OFF	2006/08/11 10:59
S4	3	@ad<"20030723" and (hash$3 adj memory) and signature and index and (memory adj array)	US-PGPUB; USPAT	OR	OFF	2006/08/11 11:00
S5	50	@ad<"20030723" and (hash$3 adj memory) and signature and index and array	US-PGPUB; USPAT	OR	OFF	2006/08/11 11:01
S6	1	@ad<"20030723" and (hash$3 adj memory) and signature and index and array and comparator	US-PGPUB; USPAT	OR	OFF	2006/08/11 11:01
S7	50	@ad<"20030723" and (hash$3 adj memory) and signature and index and array	US-PGPUB; USPAT	OR	OFF	2006/08/11 11:02
S8	65	@ad<"20030723" and (hash$3 near memory) and signature and index and array	US-PGPUB; USPAT	OR	OFF	2006/08/11 11:02
S9	3	@ad<"20030723" and (hash$3 near memory) and signature and index and (memory adj array)	US-PGPUB; USPAT	OR	OFF	2006/08/11 11:03
S10	0	@ad<"20030723" and ((hash$3 near memory) with signature) and index	US-PGPUB; USPAT	OR	OFF	2006/08/11 11:03
S11	3	@ad<"20030723" and ((hash$3 near memory) with signature)	US-PGPUB; USPAT	OR	OFF	2006/08/11 11:05
S12	864	@ad<"20030723" and (hash) and signature and (data near (search$3 retriev$4))	US-PGPUB; USPAT	OR	OFF	2008/01/07 05:51
S13	263	@ad<"20030723" and (hash adj value) and signature and (data near (search$3 retriev$4))	US-PGPUB; USPAT	OR	OFF	2006/08/11 11:08
S14	108	@ad<"20030723" and (hash adj value) and signature and (data near (search$3 retriev$4)) and array	US-PGPUB; USPAT	OR	OFF	2006/08/11 11:08
S15	7	@ad<"20030723" and (hash adj value) and signature and (data near (search$3 retriev$4)) and (memory adj array)	US-PGPUB; USPAT	OR	OFF	2006/08/11 11:12

EAST Search History

S16	5	@ad<"20030723" and ((hash adj value) same (signature) same (data near (search$3 retriev$4)))	US-PGPUB; USPAT	OR	OFF	2006/08/11 11:19
S17	1	@ad<"20030723" and ((hash$3 adj (memory data)) same (signature) same (data near (search$3 retriev$4)))	US-PGPUB; USPAT	OR	OFF	2006/08/11 11:21
S18	136	@ad<"20030723" and ((hash$3 adj (memory data)) and (signature) and (data near (search$3 retriev$4)))	US-PGPUB; USPAT	OR	OFF	2006/08/11 17:35
S19	15	@ad<"20030723" and ((hash$3 adj (memory data)) and (signature) and (data near (search$3)))	US-PGPUB; USPAT	OR	OFF	2006/08/11 17:38
S20	1	10/624533.app.	US-PGPUB; USPAT	OR	OFF	2006/08/11 15:06
S21	3	@ad<"20030723" and ((hash$3 adj (memory data)) and (signature) and (data near (search$3 retriev$4))) and 707/100.ccls.	US-PGPUB; USPAT	OR	OFF	2006/08/11 17:35
S22	8707	magnetic adj strip	US-PGPUB; USPAT	OR	OFF	2006/08/14 15:31
S23	217	(magnetic adj strip) and pore	US-PGPUB; USPAT	OR	ON	2006/08/14 15:58
S24	23	(magnetic adj strip) and pore and pole$3	US-PGPUB; USPAT	OR	ON	2006/08/14 16:04
S25	1	(magnetic adj strip) and pore and pole$3 and nanotube	US-PGPUB; USPAT	OR	ON	2006/08/14 16:00
S26	11	(magnetic adj strip) and pore and nanotube	US-PGPUB; USPAT	OR	ON	2006/08/14 16:01
S27	48	(magnetic adj strip) and nanotube	US-PGPUB; USPAT	OR	ON	2006/08/14 16:02
S28	13	(magnetic adj strip) and pore and pole$3 and tube	US-PGPUB; USPAT	OR	ON	2006/08/14 17:01
S29	9	(magnetic adj strip) and pore and pole$3 and tube and identif$6	US-PGPUB; USPAT	OR	ON	2006/08/14 17:01
S30	4307	(magnetic adj strip) same card and identif$6	US-PGPUB; USPAT	OR	ON	2006/08/14 17:04
S31	980	((magnetic adj strip) adj card) and identif$6	US-PGPUB; USPAT	OR	ON	2006/08/14 17:05
S32	236	((magnetic adj strip) adj card) same identif$6	US-PGPUB; USPAT	OR	ON	2006/08/14 17:05
S33	0	(((magnetic adj strip) adj card) same identif$6) and pore	US-PGPUB; USPAT	OR	ON	2006/08/14 17:05
S34	23	(((magnetic adj strip) adj card) same identif$6) and hole	US-PGPUB; USPAT	OR	ON	2006/08/16 13:33

EAST Search History

S35	3	(((magnetic) adj card) same identif$6) and hole and pore	US-PGPUB; USPAT	OR	ON	2006/08/16 13:31
S36	23	(((magnet$4 adj strip) adj card) same identif$6) and hole	US-PGPUB; USPAT	OR	ON	2006/08/16 13:34
S37	0	(((magnet$4 adj material) adj card) same identif$6) and hole	US-PGPUB; USPAT	OR	ON	2006/08/16 13:34
S38	17	(((magnet$4 adj material) with card) same identif$6) and hole	US-PGPUB; USPAT	OR	ON	2006/08/16 13:48
S39	1	(((magnet$4 adj film) with card) same identif$6) and hole	US-PGPUB; USPAT	OR	ON	2006/08/16 13:49
S40	4	(((magnet$4 adj film) with card) same identif$6)	US-PGPUB; USPAT	OR	ON	2006/08/16 13:56
S41	1	(((magnet$4 adj identif$6) with card) same identif$6) and hole	US-PGPUB; USPAT	OR	ON	2006/08/16 13:58
S42	17	(((magnet$4 adj identif$6) with card) same identif$6)	US-PGPUB; USPAT	OR	ON	2006/08/16 14:33
S43	0	"10625235"	US-PGPUB; USPAT	OR	ON	2006/08/16 14:33
S44	1	10/625235.app.	US-PGPUB; USPAT	OR	ON	2006/08/16 15:43
S45	1	"5480685".pn.	US-PGPUB; USPAT	OR	ON	2006/08/17 15:59
S46	194	microchip and (identif$6 near card)	US-PGPUB; USPAT	OR	ON	2006/08/17 15:59
S47	64	microchip and (identif$6 near card) and voltage	US-PGPUB; USPAT	OR	ON	2006/08/17 16:01
S48	2	microchip and (identif$6 near card) and (voltage near source)	US-PGPUB; USPAT	OR	ON	2006/08/17 16:00
S49	843	@ad<"20030723" and (hash) and (index same position)	US-PGPUB; USPAT	OR	OFF	2007/02/01 19:09
S50	393	@ad<"20030723" and (hash near table) and (index same position)	US-PGPUB; USPAT	OR	OFF	2007/02/01 19:09
S51	17	@ad<"20030723" and (hash near table) and ((index same position) with match)	US-PGPUB; USPAT	OR	OFF	2007/02/01 19:12
S52	181	@ad<"20030723" and (search$3 same (hash near table)) and ((index same position))	US-PGPUB; USPAT	OR	ON	2007/02/01 19:14
S53	8	@ad<"20030723" and ((first adj memory) and (second adj memory)) and (search$3 same (hash near table)) and ((index same position))	US-PGPUB; USPAT	OR	ON	2007/02/02 09:33
S54	1	10/624533.app.	US-PGPUB; USPAT	OR	ON	2007/02/02 09:33

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	12361	(linked adj list)	US-PGPUB; USPAT	OR	OFF	2008/01/06 14:17
S2	235	(linked adj list) near search$3	US-PGPUB; USPAT	OR	OFF	2008/01/06 15:49
S3	13	((linked adj list) near search$3) same without	US-PGPUB; USPAT	OR	OFF	2008/01/06 14:59
S4	1	(((linked adj list) near search$3) same without).clm.	US-PGPUB; USPAT	OR	OFF	2008/01/06 15:00
S5	38	(((linked adj list) and search$3) and without).clm.	US-PGPUB; USPAT	OR	OFF	2008/01/06 15:02
S6	8	(((linked adj list) near2 search$3) and without).clm.	US-PGPUB; USPAT	OR	OFF	2008/01/06 15:01
S7	1	(((linked adj list) and search$3) same without).clm.	US-PGPUB; USPAT	OR	OFF	2008/01/06 15:02
S8	0	((linked adj list) near search$3) and 713/181.ccls.	US-PGPUB; USPAT	OR	OFF	2008/01/06 15:50
S9	465	713/181.ccls.	US-PGPUB; USPAT	OR	OFF	2008/01/06 15:50
S10	6	((linked adj list) near search$3) and "713".clas.	US-PGPUB; USPAT	OR	OFF	2008/01/06 15:53
S11	49	((linked adj list) near search$3) and "711".clas.	US-PGPUB; USPAT	OR	OFF	2008/01/06 15:53
S12	66	((linked adj list) near search$3) and "707".clas.	US-PGPUB; USPAT	OR	OFF	2008/01/06 15:53

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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
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S5	38	(((linked adj list) and search$3) and without).clm.	US-PGPUB; USPAT	OR	OFF	2008/01/06 15:02
S6	8	(((linked adj list) near2 search$3) and without).clm.	US-PGPUB; USPAT	OR	OFF	2008/01/06 15:01
S7	1	(((linked adj list) and search$3) same without).clm.	US-PGPUB; USPAT	OR	OFF	2008/01/06 15:02

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1 Solving a problem using cooperating data structures   ◼

Ali A. Kooshesh
March 1995 **ACM SIGCSE Bulletin , Proceedings of the twenty-sixth SIGCSE technical symposium on Computer science education SIGCSE '95**, Volume 27 Issue 1
Publisher: ACM Press
Full text available: 📄 pdf(450.05 KB)   Additional Information: full citation, abstract, references, index terms

In this write-up, we describe series parallel graphs, a simple class of general graphs, and show how to compute their minimum spanning tree or shortest path. The problem is intended to be used as an assignment in an undergraduate course on Data Structures. It is a simple problem, yet non-trivial, and allows the students to implement several interesting data structures in one programming assignment.

2 Education software for courses in computer organization and systems programming: ◼
Assist: a self modifiable assembler for instructional purposes
J. R. Mashey, G. M. Campbell, C. Forney
August 1972 **Proceedings of the ACM annual conference - Volume 1 ACM'72**
Publisher: ACM Press
Full text available: 📄 pdf(283.98 KB)   Additional Information: full citation, abstract, references, citings

ASSIST - Assembler System for Student Instruction and Systems Teaching, is a self-modifiable IN-CORE Assembler F compatible System/360 assembler-interpreter. It allows students to replace portions of the assembler using a process which is economical and provides good diagnostics. It has the capability of assembling a subroutine written in assembler language, linking the routine to itself and then executing another program utilizing the newly linked subroutine. The replaceable subroutines perform ...

Keywords: computer science education, in-core assembler, self-modifiable assembler, student programming projects., student-oriented translator, systems programming instruction

3 CADTOOLS: a CAD algorithm development system   ◼
Eric Schell, M. Ray Mercer
June 1985 **Proceedings of the 22nd ACM/IEEE conference on Design automation DAC '85**
Publisher: ACM Press
Full text available: 📄 pdf(682.46 KB)   Additional Information: full citation, abstract, references, index terms

This paper defines a development system for computer-aided design (CAD) algorithms for combinational logic networks. The principle features of this system include graph-oriented commands and control structures, list handling facilities, and an expandable library of commonly used CAD subprograms. Application programs written using the prototype system, CADTOOLS, include worst case path delay analysis, Sandia Controllability and Observability Analysis (SCOAP) testability measure, and logic si ...

Keywords: CAD, software engineering

4   Automated analysis: Control-flow integrity

Martín Abadi, Mihai Budiu, Ûlfar Erlingsson, Jay Ligatti
November 2005 **Proceedings of the 12th ACM conference on Computer and communications security CCS '05**
Publisher: ACM Press

Full text available: pdf(218.60 KB)   Additional Information: full citation, abstract, references, citings, index terms

Current software attacks often build on exploits that subvert machine-code execution. The enforcement of a basic safety property, Control-Flow Integrity (CFI), can prevent such attacks from arbitrarily controlling program behavior. CFI enforcement is simple, and its guarantees can be established formally even with respect to powerful adversaries. Moreover, CFI enforcement is practical: it is compatible with existing software and can be done efficiently using software rewriting in commodity syste ...

Keywords: binary rewriting, control-flow graph, inlined reference monitors, vulnerabilities

5   Writing efficient programs

Jon Louis Bentley
January 1982 Book
Publisher: Prentice-Hall, Inc.
Additional Information: full citation, abstract, references, cited by, index terms

The primary task of software engineers is the cost-effective development of maintainable and useful software. There are many secondary problems lurking in that definition. One such problem arises from the term "useful": to be useful in the application at hand, software must often be efficient (that is, use little time or space). The problem we will consider in this book is building efficient software systems.

There are a number of levels at which we may confront the problem of efficien ...

6   Data access: Dali: a communication-centric data abstraction layer for energy-constrained devices in mobile sensor networks

Christopher M. Sadler, Margaret Martonosi
June 2007 **Proceedings of the 5th international conference on Mobile systems, applications and services MobiSys '07**
Publisher: ACM Press
Full text available: pdf(532.76 KB)   Additional Information: full citation, abstract, references, index terms

Communications in mobile and frequently-disconnected sensor networks are characterized by low-bandwidth radios, unreliable links, and disproportionately high energy costs compared to other system operations. Therefore, we must use as efficiently as possible any periods of connectivity that we have. For this reason, nodes in these networks need mechanisms that organize data to streamline search operations, local computation, and communications.

This work proposes a Data Abstraction Laye ...

Keywords: data search and storage, energy efficient communications, mobile ad hoc sensor networks

7 Deadlock detection and resolution in simulation models
Murali Krishnamurthi, Amar Basavatia, Sanjeev Thallikar
December 1994 **Proceedings of the 26th conference on Winter simulation WSC '94**
Publisher: Society for Computer Simulation International
Full text available: pdf(748.12 KB)   Additional Information: full citation, references, citings, index terms

8 Runtime reliability mechanisms: XFI: software guards for system address spaces
Úlfar Erlingsson, Martín Abadi, Michael Vrable, Mihai Budiu, George C. Necula
November 2006 **Proceedings of the 7th symposium on Operating systems design and implementation OSDI '06**
Publisher: USENIX Association
Full text available: pdf(199.86 KB)   Additional Information: full citation, abstract, references

XFI is a comprehensive protection system that offers both flexible access control and fundamental integrity guarantees, at any privilege level and even for legacy code in commodity systems. For this purpose, XFI combines static analysis with inline software guards and a two-stack execution model. We have implemented XFI for Windows on the x86 architecture using binary rewriting and a simple, stand-alone verifier; the implementation's correctness depends on the verifier, but not on the rewrite ...

9 Firmware/hardware support for operating systems: principles and selected history
Gary H. Sockut
December 1975 **ACM SIGMICRO Newsletter**, Volume 6 Issue 4
Publisher: ACM Press
Full text available: pdf(691.28 KB)   Additional Information: full citation, abstract, references

Firmware/hardware support for operating systems is described briefly, and proposed criteria for determining which operating system functions are the best candidates for firmware/hardware implementation are listed. A selected history of the area is presented in three sections: past and current research in support for virtual machines and two sections on past and current research in support for non -- virtual machine operating system functions.

10 Common perceptions of computing: What is computing?: bridging the gap between teenagers' perceptions and graduate students' experiences
Sarita Yardi, Amy Bruckman
September 2007 **Proceedings of the third international workshop on Computing education research ICER '07**
Publisher: ACM Press
Full text available: pdf(346.02 KB)   Additional Information: full citation, abstract, references, index terms

Studies show that teenagers perceive computing to be boring, antisocial, and irrelevant to their lives. We interviewed 13 teenagers from local Atlanta schools and observed over 40 teenagers in after-school technology programs to learn more about their perceptions of computing. We then interviewed 22 graduate students in the Human-Centered Computing and Human-Computer Interaction programs at Georgia Tech in order to learn about the factors that motivated them to pursue degrees in computing. We ...

Keywords: HCI, broadening participation, computer science education, culture, curriculum, teenagers

11 Allocation and compaction - a mathematical model for memory management

Dennis W. Ting

March 1976 **Proceedings of the 1976 ACM SIGMETRICS conference on Computer performance modeling measurement and evaluation SIGMETRICS '76**

Publisher: ACM Press

Full text available: pdf(515.20 KB)   Additional Information: full citation, abstract, references, index terms

A mathematical model for the processes of allocation and compaction is developed. The behavior of the most commonly used compaction algorithm is studied under this model. The relationship between time and space and trade-offs between "conservative" and "non-conservative" compaction algorithms are also investigated.

12 Multiple source shortest paths in a genus g graph

Sergio Cabello, Erin W. Chambers

January 2007 **Proceedings of the eighteenth annual ACM-SIAM symposium on Discrete algorithms SODA '07**

Publisher: Society for Industrial and Applied Mathematics

Full text available: pdf(651.20 KB)   Additional Information: full citation, abstract, references

We give an $O(g^2 n \log n)$ algorithm to represent the shortest path tree from all the vertices on a single specified face f in a genus g graph. From this representation, any query distance from a vertex in f can be obtained in $O(\log n)$ time. The algorithm uses a kinetic data structure, where the source of the tree iteratively moves across edges in f. In addition, we give applications using these shortest path trees i ...

13 Skip lists: a probabilistic alternative to balanced trees

William Pugh

June 1990 **Communications of the ACM**, Volume 33 Issue 6

Publisher: ACM Press

Full text available: pdf(913.49 KB)   Additional Information: full citation, abstract, references, citings, index terms, review

Skip lists are data structures that use probabilistic balancing rather than strictly enforced balancing. As a result, the algorithms for insertion and deletion in skip lists are much simpler and significantly faster than equivalent algorithms for balanced trees.

Keywords: data structures, searching, trees

14 A Model-Based Approach for Executable Specifications on Reconfigurable Hardware

Tim Schattkowsky, Wolfgang Mueller, Achim Rettberg

March 2005 **Proceedings of the conference on Design, Automation and Test in Europe - Volume 2 DATE '05**

Publisher: IEEE Computer Society

Full text available: pdf(174.47 KB)   Additional Information: full citation, abstract, index terms

UML 2.0 provides a rich set of diagrams for systems documentation and specification. Many efforts have been undertaken to employ different aspects of UML for multiple domains, mainly in the area of software systems. Considering the area of electronic design automation, however, we currently see only very few approaches, which investigate UML for hardware design and hardware/software co-design. In this article, we present an approach for executable UML closing the gap from system specification to ...

15 Contention: Efficient multi-word locking using randomization

Phuong Hoai Ha, Philippas Tsigas, Mirjam Wattenhofer, Roger Wattenhofer

July 2005 **Proceedings of the twenty-fourth annual ACM symposium on Principles of distributed computing PODC '05**

Publisher: ACM Press

Full text available: pdf(209.11 KB)   Additional Information: full citation, abstract, references, index terms

In this paper we examine the general multi-word lock problem, where processes are allowed to multilock arbitrary registers. Aiming for a highly efficient solution we propose a randomized algorithm which successfully breaks long dependency chains, the crucial factor for slowing down an execution. In the analysis we focus on the 2-word lock problem and show that in this special case an execution of our algorithm takes with high probability at most time $O(\Delta^3 \log n / \log \log n)$, where ...

Keywords: dining philosophers, multi-word locking, randomization, shared memory

16 Small-scale software components

James E. Emery

October 1979 **ACM SIGSOFT Software Engineering Notes**, Volume 4 Issue 4

Publisher: ACM Press

Full text available: pdf(288.26 KB)   Additional Information: full citation, abstract, references, citings

An empirical study to identify standardizable small-scale software components from existing source code is described. The rationale for such an effort is presented, followed by a description of the specific study performed by the author. Component identification methods are presented along with preliminary findings. Future identification efforts and possible notational schemes are also discussed.

17 An experience with parallelism in Ada

David S. Notkin

November 1980 **ACM SIGPLAN Notices , Proceeding of the ACM-SIGPLAN symposium on Ada programming language SIGPLAN '80**, Volume 15 Issue 11

Publisher: ACM Press

Full text available: pdf(620.32 KB)   Additional Information: full citation, abstract, references, citings

One of the more interesting and controversial features of Ada is the tasking structure. The Ada tasking facility provides high level mechanisms for communication and synchronization among tasks executing in parallel. The open question about these mechanisms is whether they provide programmers with both the appropriate level of abstraction and also the necessary level of control.This paper describes in detail the implementation of a system using parallelism that was written during the Ada test an ...

18 Why matrix manipulation should be done in C++

W. Douglas Maurer

April 2004 **Journal of Computing Sciences in Colleges**, Volume 19 Issue 4

Publisher: Consortium for Computing Sciences in Colleges

Full text available: pdf(187.76 KB)   Additional Information: full citation, abstract, references

We show here how the technique of overloaded function call in C++ may be used in making matrix manipulations easier to understand, even when these involve specialized matrix representations. Such representations include sparse matrices, packed Boolean matrices, tridiagonal matrices, and arrays of pointers to arrays; and these may start with m_{00} or with m_{11}. In all these cases, we may use the notation m (i, j) (with parentheses, not square brackets) to denote m_{ij} ...

19 Software caching vs. prefetching

Aneesh Aggarwal

June 2002 **ACM SIGPLAN Notices , Proceedings of the 3rd international symposium on Memory management ISMM '02**, Volume 38 Issue 2 supplement

Publisher: ACM Press

Full text available: pdf(150.77 KB)   Additional Information: full citation, abstract, references, index terms

The performance gap between memory subsystem and high-performance processors is ever-increasing. Prefetching is one method to bridge this performance gap. Prefetching has been proposed for array-based and pointer applications, typically using software-based techniques with the help of the compiler. Prefetching suffers from certain disadvantages such as an increase in memory traffic, an increase in the number of executed instructions, and an increase in memory requirement for some cases.In this p ...

Keywords: memory optimization, software caching, software prefetching

20 On self-organizing sequential search heuristics

Ronald Rivest

February 1976 **Communications of the ACM**, Volume 19 Issue 2

Publisher: ACM Press

Full text available: pdf(487.30 KB)   Additional Information: full citation, abstract, references, citings

This paper examines a class of heuristics for maintaining a sequential list in approximately optimal order with respect to the average time required to search for a specified element, assuming that each element is searched for with a fixed probability independent of previous searches performed. The "move to front" and "transposition" heuristics are shown to be optimal to within a constant factor, and the transposition rule is shown to be the more efficient of the two ...

Keywords: heuristics, list-processing, searching, self-organizing

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☐ **1.** **Design of multi-invariant data structures for robust shared accesses in multiprocessor systems**
I-Ling Yen; Bastani, F.B.; Taylor, D.J.;
Software Engineering, IEEE Transactions on
Volume 27, Issue 3, March 2001 Page(s):193 - 207
Digital Object Identifier 10.1109/32.910857

AbstractPlus | References | Full Text: PDF(276 KB)   IEEE JNL
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☐ **2.** **Parallel multiple hashing for packet classification**
Hyesook Lim; Hye-Ran Kim; Yeo-Jin Jung;
High Performance Switching and Routing, 2005. HPSR. 2005 Workshop on
12-14 May 2005 Page(s):104 - 107
Digital Object Identifier 10.1109/HPSR.2005.1503203

AbstractPlus | Full Text: PDF(275 KB)   IEEE CNF
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☐ **3.** **Memory organization in a real-time database based on red-black tree structure**
Jianwei Li; Yubin Xu; Hong Guo;
Intelligent Control and Automation, 2004. WCICA 2004. Fifth World Congress on
Volume 5, 15-19 June 2004 Page(s):3971 - 3974 Vol.5

AbstractPlus | Full Text: PDF(300 KB)   IEEE CNF
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☐ **4.** **Empirical studies on the Internet**
Benesch, T.;
Information Technology Interfaces, 2000. ITI 2000. Proceedings of the 22nd International Conference on
13-16 June 2000 Page(s):197 - 204

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☐ **5.** **A trie-based algorithm for IP lookup problem**
Yilmaz, P.A.; Belenkiy, A.; Uzun, N.; Gogate, N.; Toy, M.;
Global Telecommunications Conference, 2000. GLOBECOM '00. IEEE
Volume 1, 27 Nov.-1 Dec. 2000 Page(s):593 - 598 vol.1
Digital Object Identifier 10.1109/GLOCOM.2000.892085

AbstractPlus | Full Text: PDF(548 KB)   IEEE CNF
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6. **An efficient management method of moving spatial objects**
Nakamura, Y.; Dekihara, H.;
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